

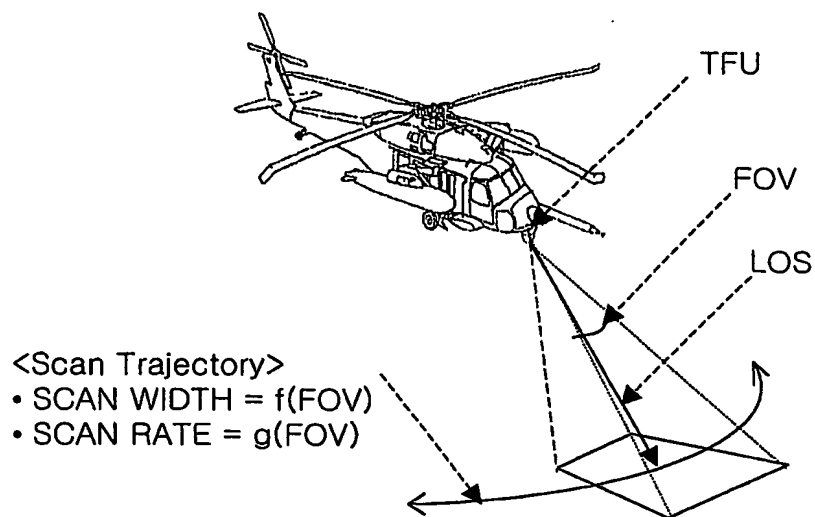
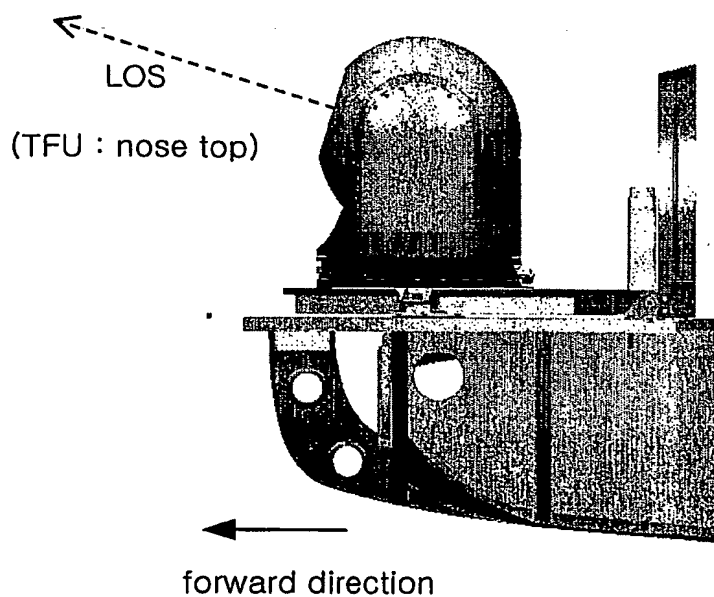
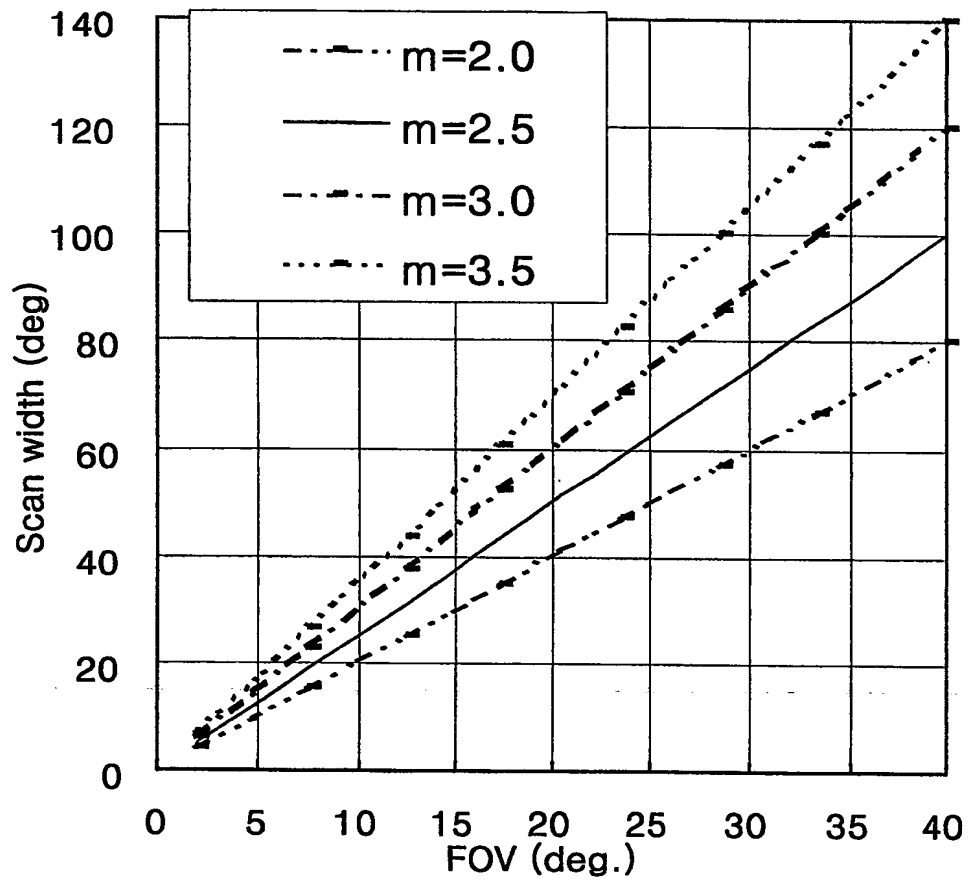
1/15  
Fig. 1A

Fig. 1B



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2/15  
Fig. 2

3/15

Fig. 3B

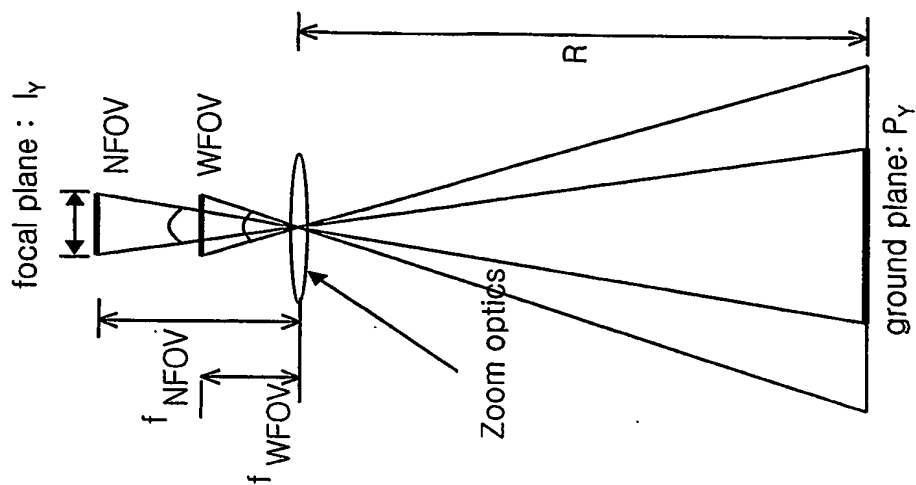
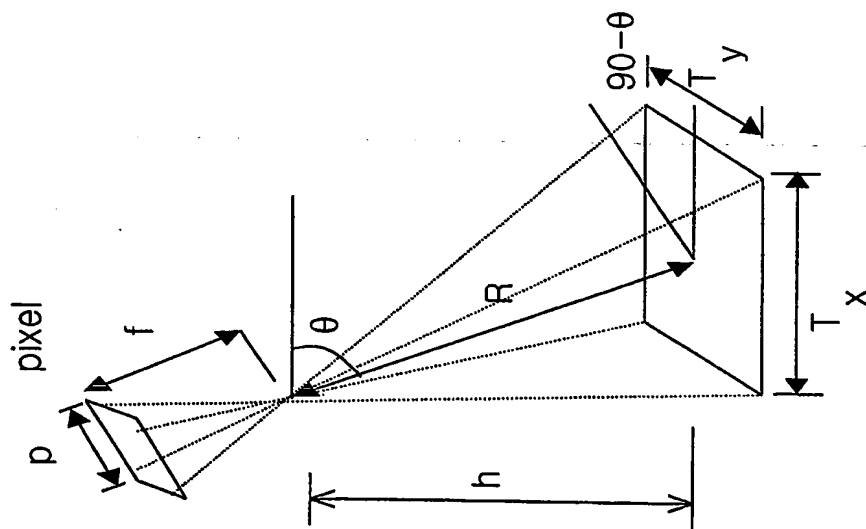
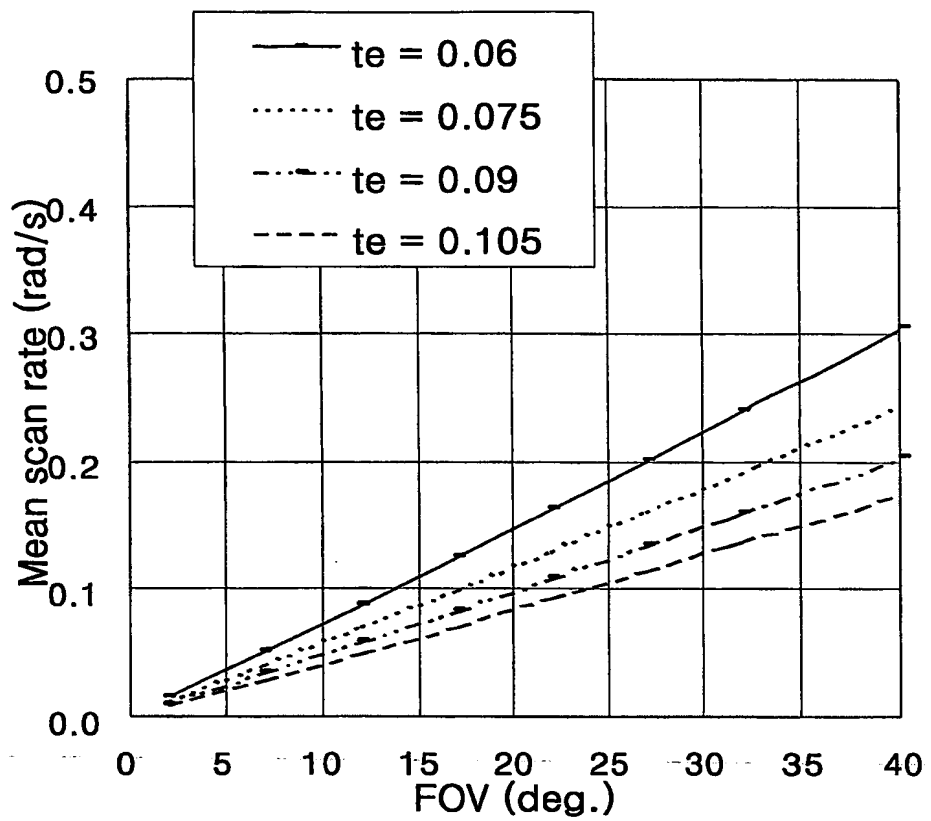


Fig. 3A



4/15  
Fig. 4

5/15

Fig. 5

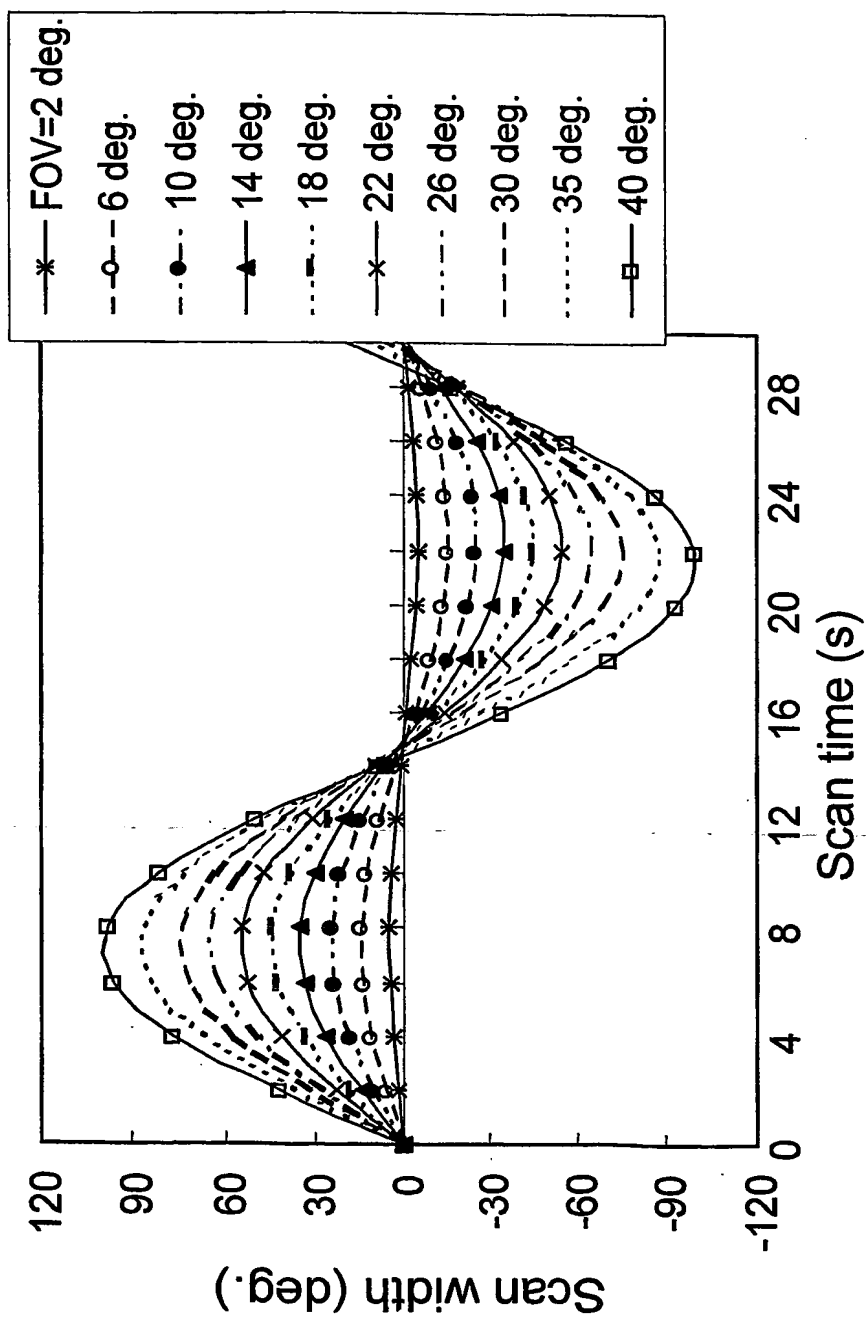
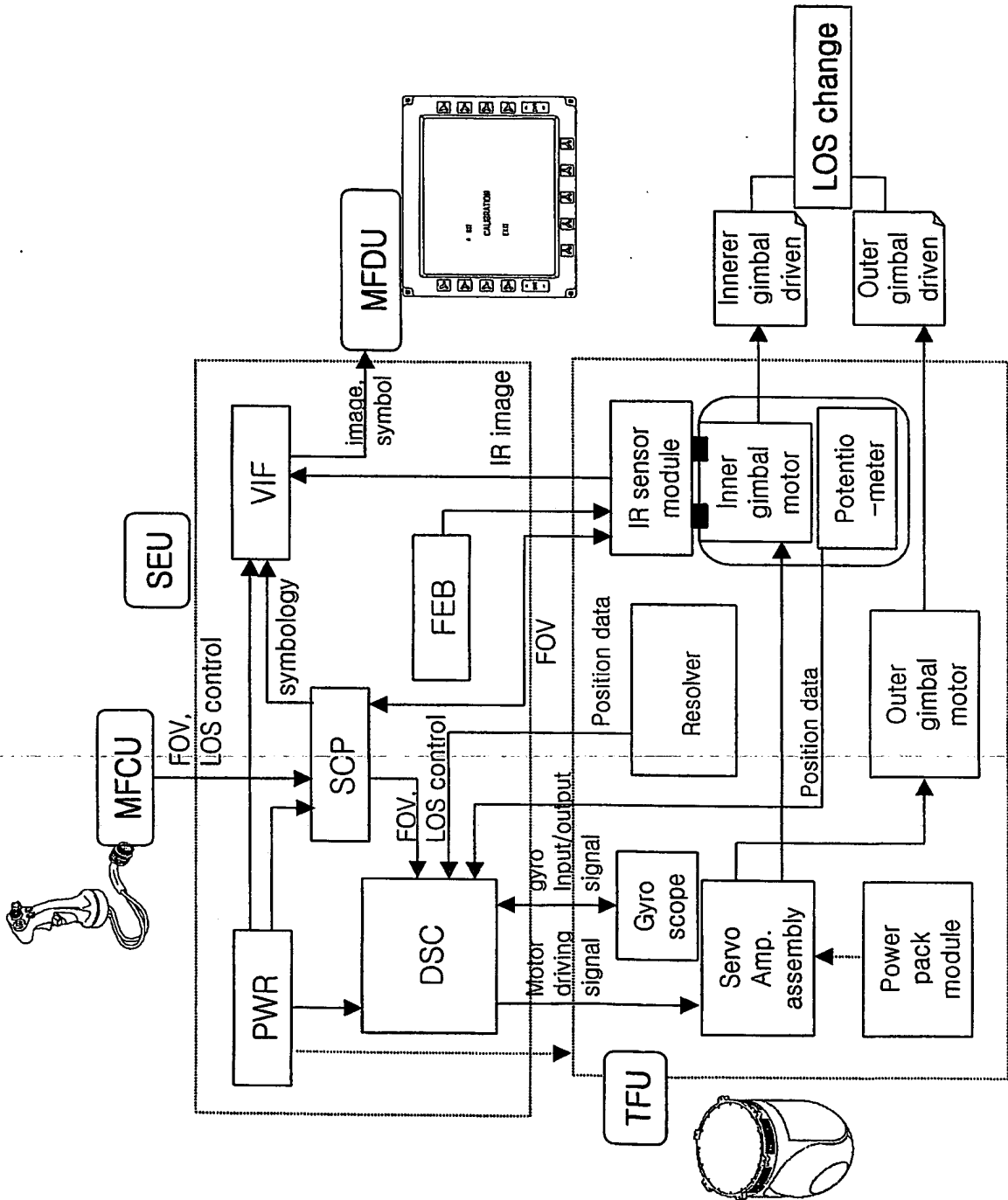
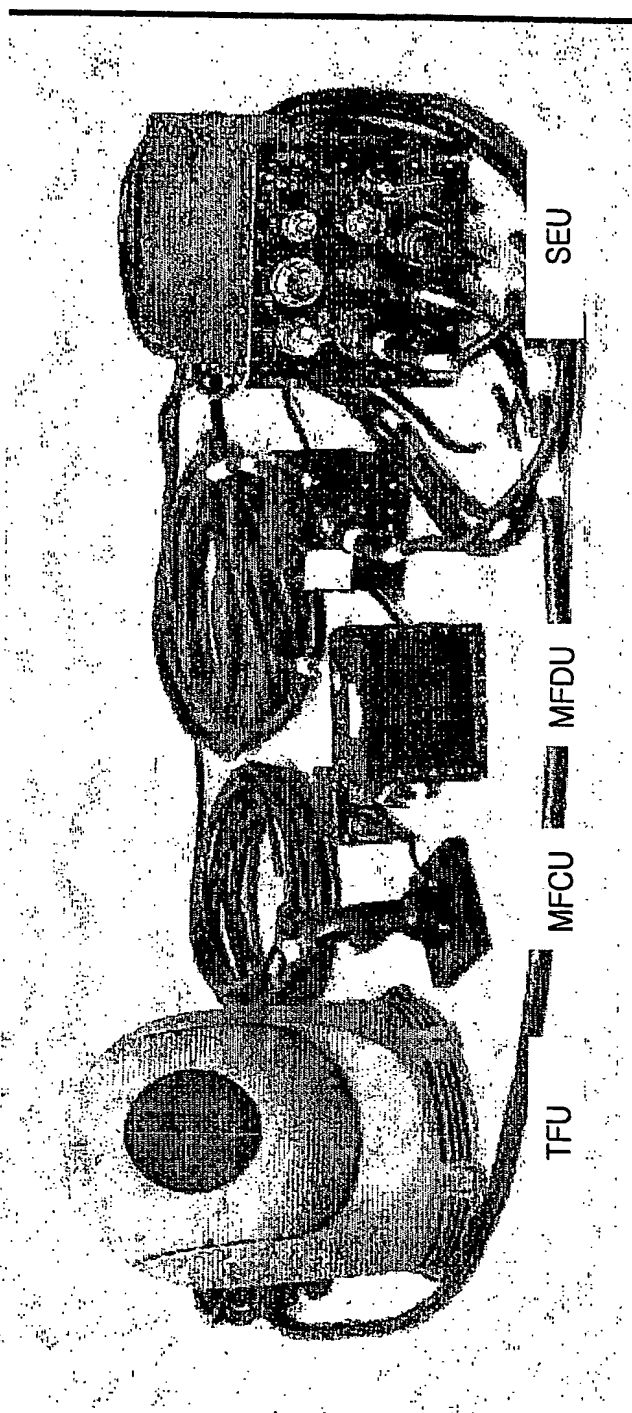


Fig. 6

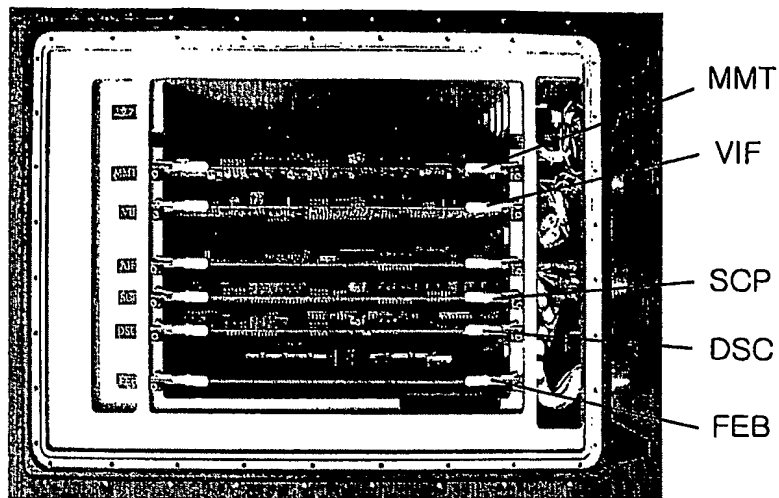


7/15

Fig. 7

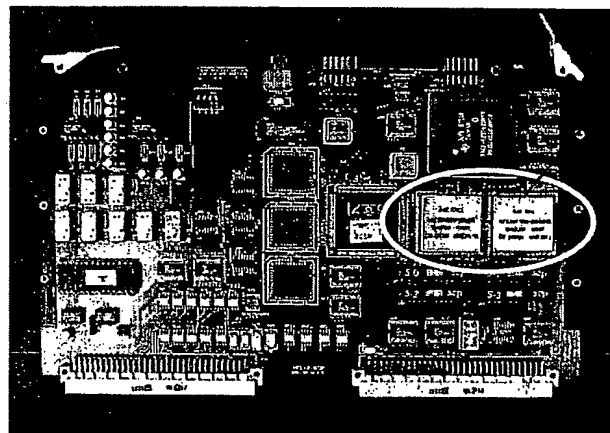


8/15  
Fig. 8

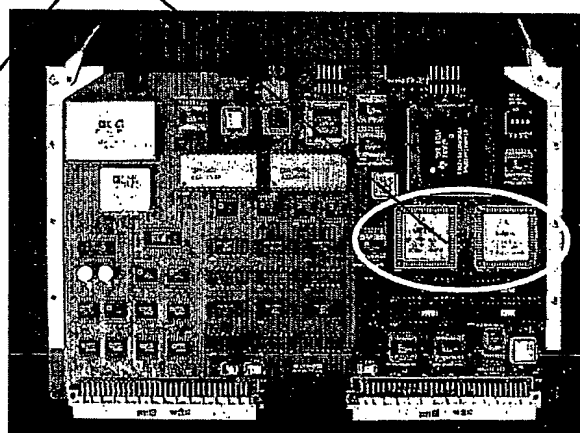


(a) SEU

flash memory  
(EPROM)



(b) SCP

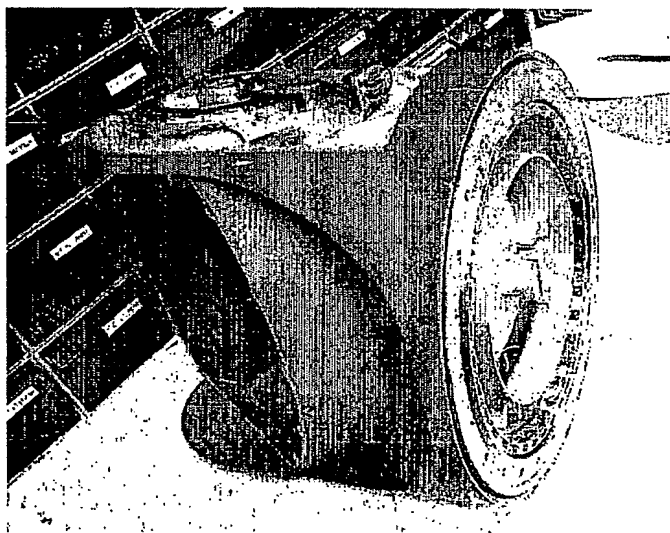
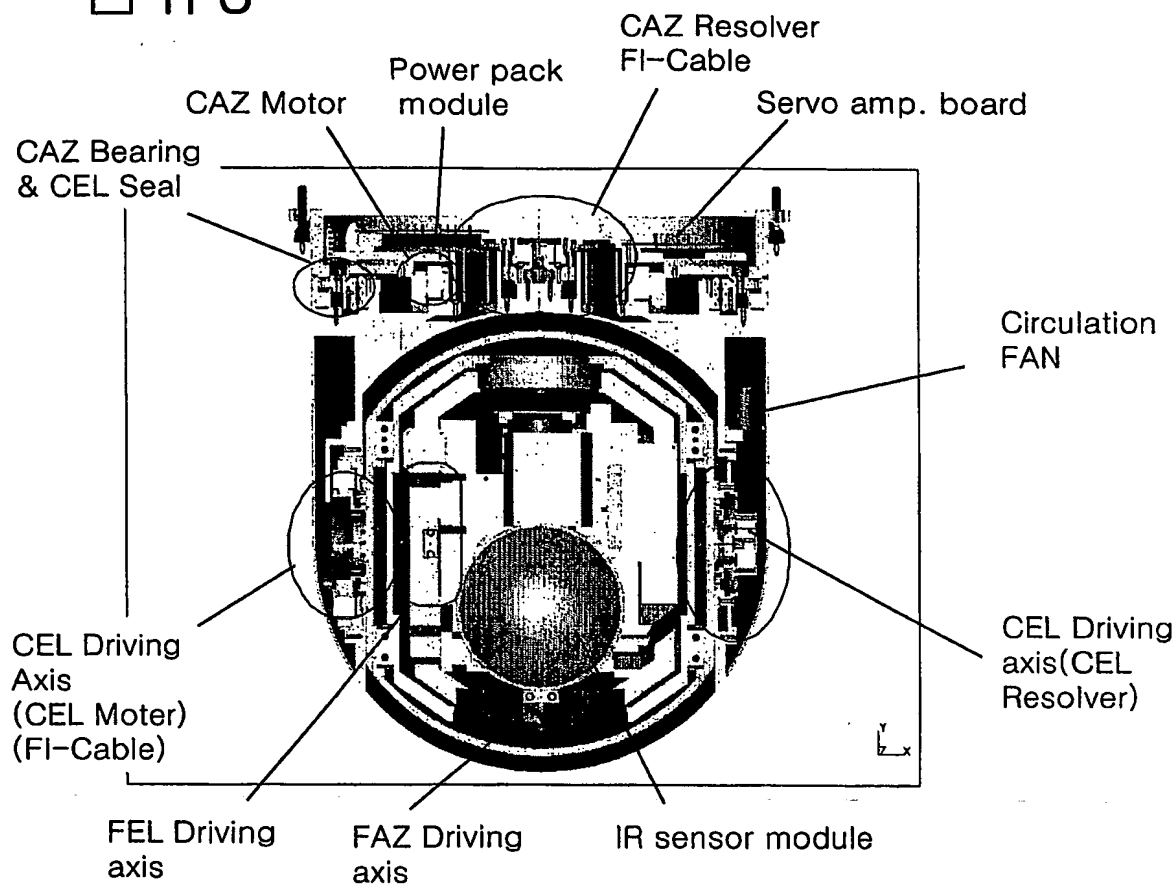


(c) DSC



9/15  
Fig. 9

□ TFU



10/15  
Fig. 10A

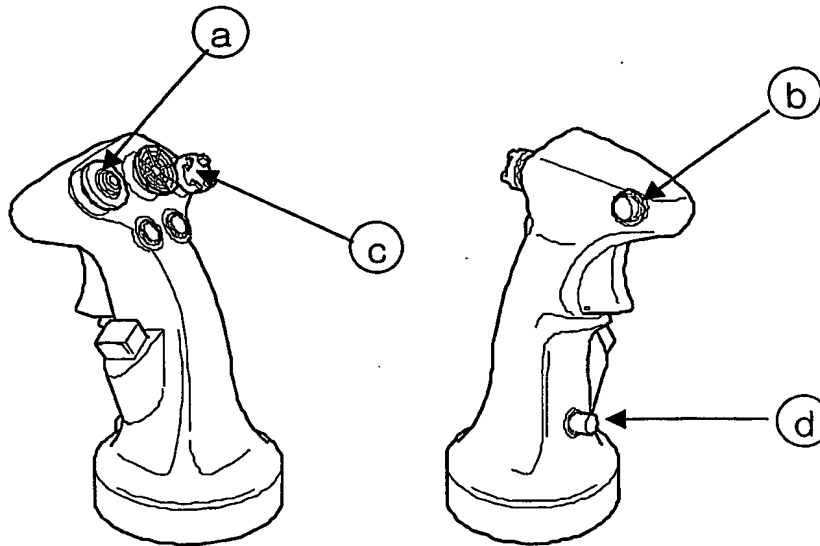
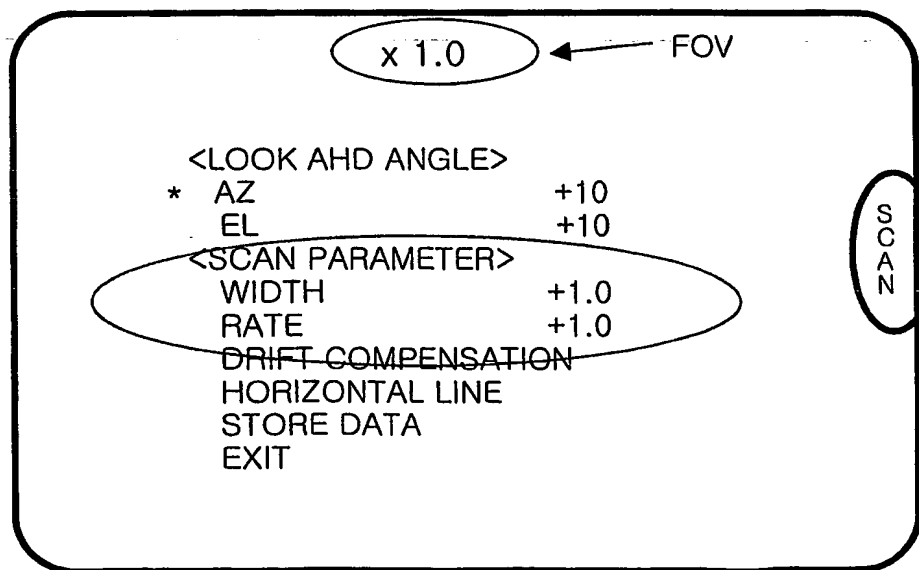
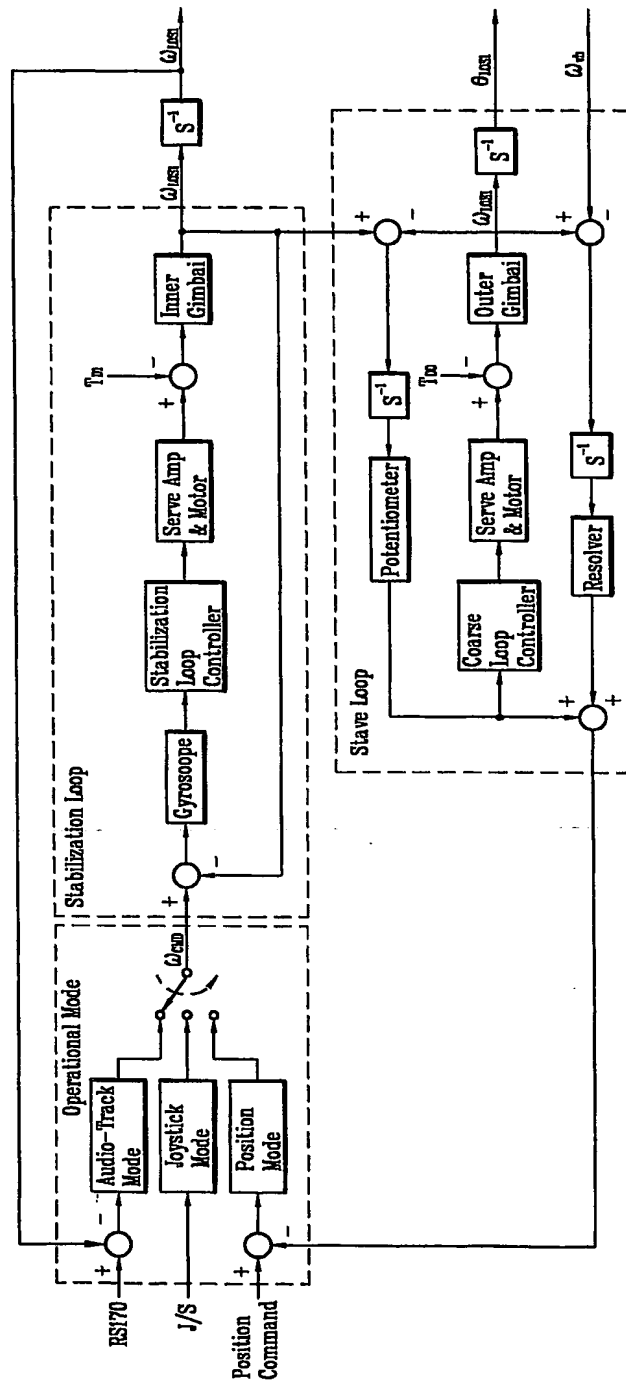


Fig. 10B



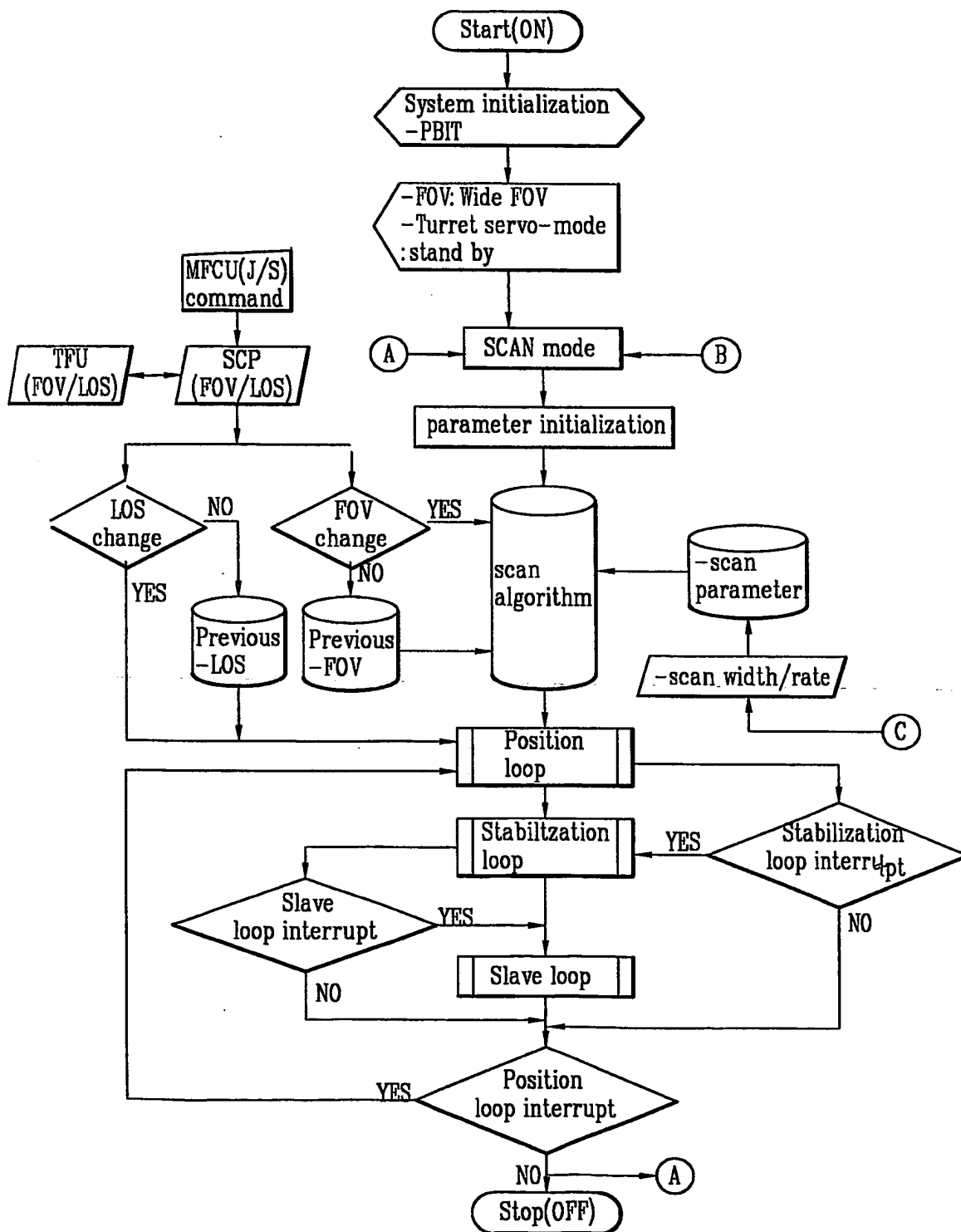
11/15

FIG. 11



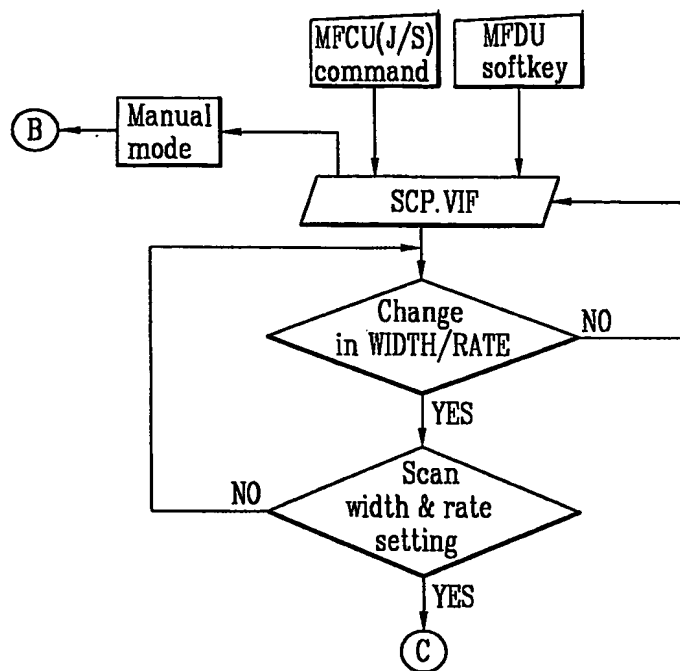
12/15

FIG. 12A



13/15

FIG. 12B



14/15

Fig. 13A

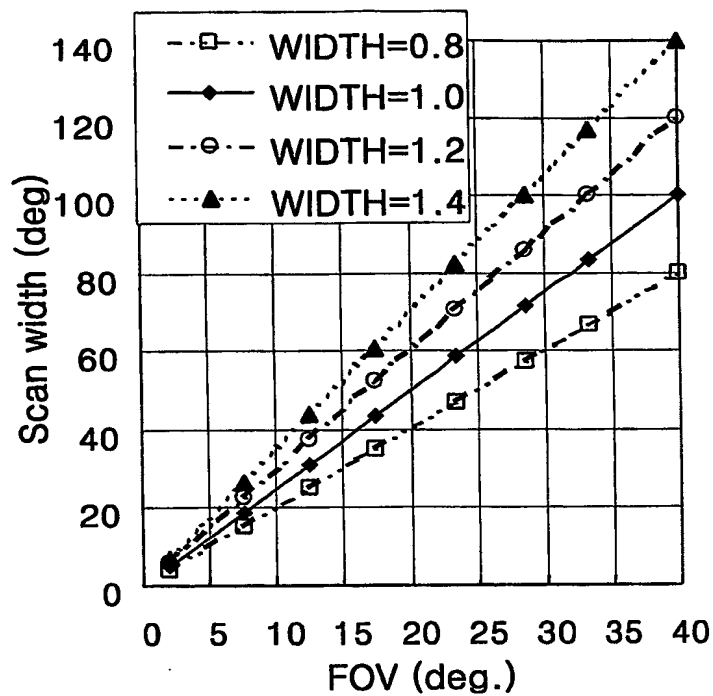
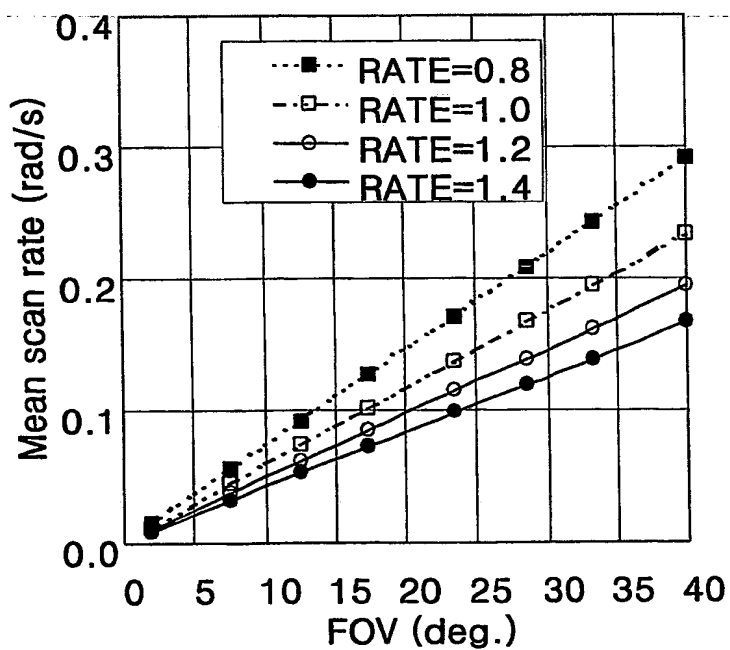


Fig. 13B



15/15  
Fig. 14A

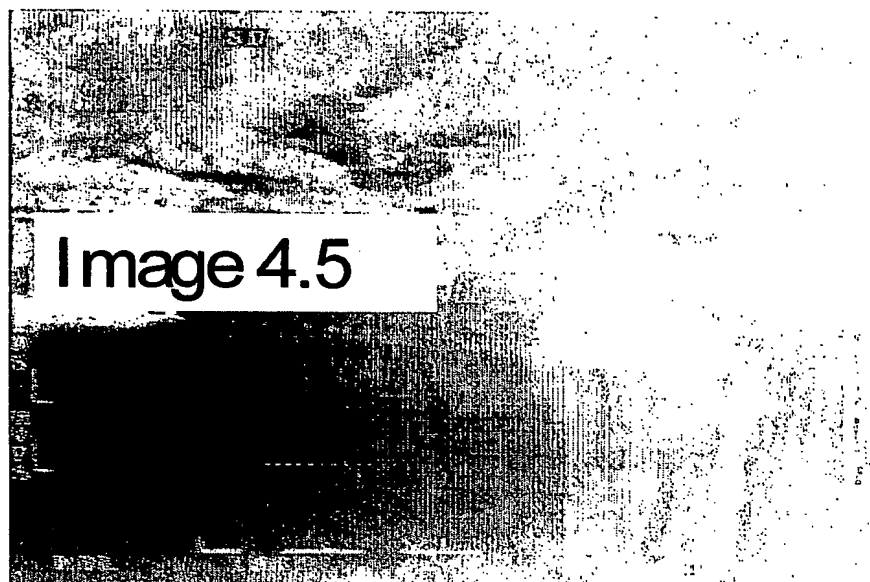
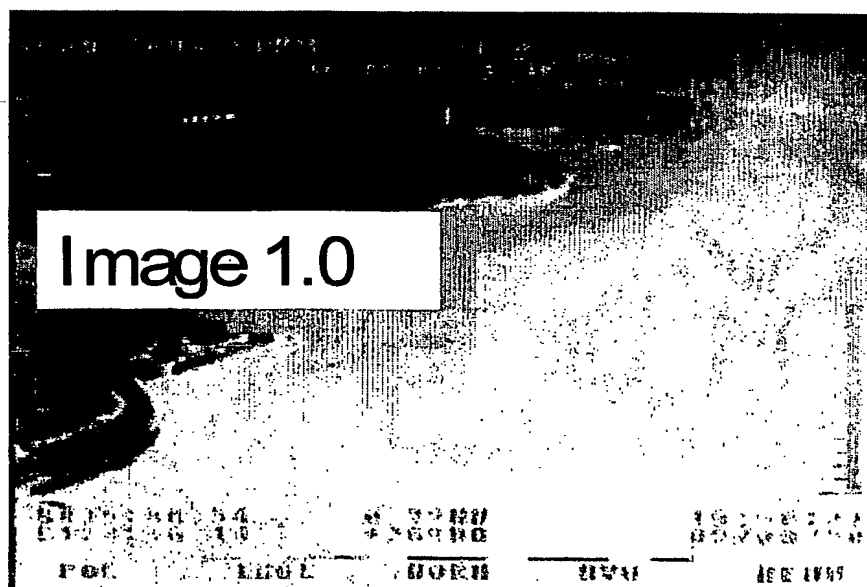


Fig. 14B



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